

Docket No.: 60188-778

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Masahiro FUKUI, et al.

Serial No.: Divisional of Appl. No.
10/085,009

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Group Art Unit:

Filed: March 26, 2004

Examiner:

For: WIRING METHOD IN LAYOUT DESIGN OF SEMICONDUCTOR INTEGRATED
CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT AND FUNCTIONAL MACRO

PRELIMINARY AMENDMENT

Mail Stop Preliminary Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please amend the application as follows: